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Attorney's Docket No.: 42P15685

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Scott A. Hareland et al.

Examiner: Unassigned

Application No.: 10/607,769

Art Unit: Unassigned

Filed: June 27, 2002

For: NONPLANAR SEMICONDUCTOR  
DEVICE WITH PARTIALLY OR FULLY  
WRAPPED AROUND GATE ELECTRODE  
AND METHODS OF FABRICATION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on June 21, 2004.

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Teresa Mattox

(Typed or printed name of person mailing correspondence)

Teresa Mattox

(Signature of person mailing correspondence)

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/21, 2004

  
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STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet

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of

|                        |                   |
|------------------------|-------------------|
| Application Number     | 10/607,769        |
| Filing Date            | June 27, 2003     |
| First Named Inventor:  | Scott A. Hareland |
| Art Unit               | Unassigned        |
| Examiner Name          | Unassigned        |
| Attorney Docket Number | 42P15685          |

[illegible]

| Examiner<br>Initials* | Cite<br>No. <sup>1</sup> | Foreign Patent Document  | Publication<br>Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns,<br>Lines, Where Relevant<br>Passages or Relevant<br>Figures Appear | T <sup>6</sup> |
|-----------------------|--------------------------|--|-----------------------------------|--|--|----------------|
|                       |                          | Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup><br>(if known) |                                   |  |  |                |
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Examiner  
Signature

Date Considered

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[illegible]

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